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NO. 021 001

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Joseph P. Lally	(Depositor's name)
	(Signature)
December 23, 2004	(Date)

12/27/2004 JADD02 00000021 10624203

01 FC:1501

1400.00 DP

02 FC:1504

300.00 DP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/624,203	07/21/2003	Paul A. Grudowski	SCI2430TP	7732

TITLE OF INVENTION: TRANSISTOR SIDEWALL SPACER STRESS MODULATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$300	\$1670	01/04/2005
EXAMINER		ART UNIT	CLASS-SUBCLASS		
TOLEDO, FERNANDO L		2823	438-218000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. Joseph P. Lally

2.

3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Freescale Semiconductor, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Austin, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date December 23, 2004

Typed or printed name

Joseph P. Lally

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